



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Bo Yeoun Jo

Serial No.: 10/750,246

Filed: 12/31/2003

For: Methods of Manufacturing

Semiconductor Devices Having Capacitors

Group Art Unit: 2812

Examiner: Not Known

) I hereby certify that this Patent

Application Transmittal and the

) documents referred to as enclosed

) therewith are being deposited with the

United States Postal Service on June 29,

2004 in an envelope addressed to

Mail Stop Missing Parts

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

utilizing the "Express Mail Post Office

to Addressee" service of the United

States Postal Service under Mailing

Label No.

EV 309 992 332 U

Charissa D. Wheeler

Preliminary Amendment

Mail Stop Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please substitute the drawings attached hereto (3 sheets; FIGS. 1A-2) for the drawings as originally attached to the specification of the above-referenced patent application.

Respectfully submitted,

Grossman & Flight, LLC 20 N. Wacker Drive, Suite 4220 Chicago, Illinois 60606 312.580.1094

June 29, 2004

By:

Mark C. Zimmerman

Reg. No. 44,006

Attorney for Applicant